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 Volume 12, Issue 4, Oct. 1997 Page(s):0_6 - 1_55
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AbstractPlus | Full Text: [PDF\(9932 KB\)](#) IEEE JNL
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IEEE Std 1204-1997
 31 Dec. 1997
AbstractPlus | Full Text: [PDF\(1240 KB\)](#) IEEE STD

- ☐ **3. Subject Index**
Power Systems, IEEE Transactions on
 Volume 12, Issue 4, Nov. 1997 Page(s):21 - 75
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- ☐ **4. Electrical characteristics of interconnections for high-performance systems**
 Deutsch, A.;
Proceedings of the IEEE
 Volume 86, Issue 2, Feb. 1998 Page(s):315 - 357
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- ☐ **5. Analog synthesis of nonlinear functions based on fuzzy logic**
 Franchi, E.; Manaresi, N.; Rovatti, R.; Bellini, A.; Baccarani, G.;
Solid-State Circuits, IEEE Journal of
 Volume 33, Issue 6, June 1998 Page(s):885 - 895
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AbstractPlus | [References](#) | Full Text: [PDF\(336 KB\)](#) IEEE JNL
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- ☐ **6. Subject Index**
Energy Conversion, IEEE Transactions on
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Simulation techniques for mixed analog/digital circuits - group of 2 »

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EL Acuna, JP Dervenis, AJ Pagones, FL Yang, RA ... - Solid-State Circuits, IEEE Journal of, 1990
- [ieeexplore.ieee.org](#)

... iSPLICE3 for the analysis of mixed analog/digital circuits ... hierarchical schematic capture package called iSPI (**Simulation Program Interface**) for design ...

[Cited by 18](#) - [Web Search](#)

A real time digital simulator for testing relays - group of 2 »

PG McLaren, R Kuffel, R Wierckx, J Giesbrecht, L ... - Power Delivery, IEEE Transactions on, 1992 - [ieeexplore.ieee.org](#)

... Whereas **analog** simulators use scaled down models of system elements, a **digital simulator** models these ... card (IRC) and one workstation **interface** card (WIC ...

[Cited by 52](#) - [Web Search](#)

A physical and scalable IV model in BSIM3v3 for analog/digital circuit simulation - group of 3 »

Y Cheng, MC Jeng, Z Liu, J Huang, M Chan, K Chen, ... - Electron Devices, IEEE Transactions on, 1997 - [ieeexplore.ieee.org](#)

... present the - model in BSIM3v3 for use in the **analog/digital circuit simulation**. ...

function of body bias, channel length and the **interface** state density ...

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Mixed Analog-digital Highly-sensitive Sensor Interface Circuit For Low-cost Microsensors - group of 5 »

A Burstein, WJ Kaiser - Solid-State Sensors and Actuators, 1995 and Eurosensors IX. ..., 1995 - [ieeexplore.ieee.org](#)

... June 25—29, 1995 MIXED **ANALOG-DIGITAL** HIGHLY-SENSITIVE ... photo: feedback bridge (a),

digital clock generator ... 10-8. Experimental and **simulation** results verify ...

[Cited by 20](#) - [Web Search](#)

All Verilog mixed-signal simulator with analog behavioral and noisemodels - group of 2 »

MK Mayes, SW Chin - VLSI Circuits, 1996. Digest of Technical Papers., 1996 ..., 1996 - [ieeexplore.ieee.org](#)

... partition **interface** slow down the **simulator**, and SPICE type simulators used for

the **analog** section are inherently slow when compared to the **digital simulator**. ...

[Cited by 12](#) - [Web Search](#)

VHDL-AMS-a hardware description language for analog and mixed-signal applications - group of 2 »

E Christen, K Bakalar, A Inc, OR Beaverton - ... and Systems II: **Analog** and **Digital** Signal Processing, IEEE ..., 1999 -

[ieeexplore.ieee.org](#)

... and cathode as two **interface** terminals of ... correctly, while providing for an efficient

simulation. ... a hardware description language for **analog**, **digital**, and mixed ...

[Cited by 88](#) - [Web Search](#) - [BL Direct](#)

Real-time digital simulator for power system analysis on a hypercube computer - group of 4 »

H Taoka, I Iyoda, H Noguchi, N Sato, T Nakazawa - Power Systems, IEEE Transactions on, 1992 - [ieeexplore.ieee.org](#)

... Numerical Analysis, Hypercube Computer, Heal—Time **Simulator**. Parallel Processing,

Man—Machine **interface**, **Analog—Digital** Converter INTRODUCTION In order ...

[Cited by 14](#) - [Web Search](#)

Simulation of mixed switched-capacitor/digital networks with signal-driven switches - group of 3 »

K Suyama, SC Fang, YP Tsvetidis - Solid-State Circuits, IEEE Journal of, 1990 - [ieeexplore.ieee.org](#)

... a comparator for an **interface** from the **analog** to the logic section, and switches

controlled by the logic. The **digital** logic used in the **simulation** consists of ...

[Cited by 24](#) - [Web Search](#)

VHD e LDO: A new mixed mode simulation

H El Tahawy, D Rodriguez, S Garcia-Sabido, JJ ... - Design Automation Conference, 1993, with EURO-VHDL'93. ..., 1993 - [ieeexplore.ieee.org](#)

... Unified modeling language Most **analog** simulators use ... language), while the **digital** simulators can ... user **interface**, library construction, **simulation** debugging and ...